

S/N Unknown

PATENT

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

Applicant: Wendell P. Noble et al.  
Serial No.: Unknown  
Filed: Herewith *al551027*  
Title: CIRCUIT AND METHOD FOR A FOLDED BIT LINE MEMORY CELL  
WITH VERTICAL TRANSISTOR AND TRENCH CAPACITOR

Examiner: Unknown  
Group Art Unit: Unknown  
Docket: 303.379US2

PRELIMINARY AMENDMENT

*a / Wncl. andt.*  
*A*  
*E. Willis*  
*10-26-00*

Box Patent Application  
Assistant Commissioner for Patents  
Washington, D.C. 20231

Before taking up the above identified application for examination, please amend it as follows:

IN THE SPECIFICATION

At the first line after the title, insert thereto: *This application is a Divisional of U.S.*

*A1*  
Application No. 08/939,742, filed October 6, 1997.

IN THE CLAIMS

Please cancel claims 1-19. Claims 20-26 therefore remain pending in the application.

Applicant will file additional claims in a Supplemental Preliminary Amendment. The Examiner is invited to contact Applicants' representatives at the below-listed telephone number if examination begins before receiving the additional claims.

"Express Mail" mailing label number: *EL584269390US* Respectfully submitted,

Date of Deposit: *April 17, 2000*

I hereby certify that this paper or fee is being deposited with the United States Postal Service "Express Mail Post Office to Address" service under 37 CFR 1.10 on the date indicated above and is addressed to the Assistant Commissioner for Patents, Washington, D.C. 20231

Printed Name: *J. Shown Hise*

Signature: *[Signature]*

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By their Representatives,

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Date *April 17, 2000*

By *Daniel J. Kluth*  
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